



DWG NO		PKGS000003		REV		J		 <b>Carsem</b> Your Technology Partner	
TOLERANCES (INCH/MM)		FINISHING		SCALE		20:1		TITLE	
FRACTION		SYMBOL		ORIGINATOR				MINI SOIC 8LD	
DECIMALS				REQUESTOR				PACKAGE OUTLINE	
X				TOOLING DEPT MANAGER				REF : CUSTOMER SPECIFICATION	
XX				QA DEPT MANAGER				SHEET NO : 1 OF 2	
XXX				CTC DESIGN DEPT MANAGER					
XXXX									
REV	POS	DESCRIPTION	DATE	BY	APPD	±	ANGLE	±	
J	—	EXPOSED PAD OPTION	2006-10-17	SR 100	—				
I	—	CHANGED LOGO	2006-10-18-09	SR 100	—				

SYMBOL	MINI SOIC 8LD	
	PACKAGE OUTLINE (MILLIMETER)	±TOL
A	1.10	MAX
A1	0.10	±0.05
A2	0.86	±0.08
D	3.00	±0.10
D2	2.95	±0.10
E	4.90	±0.15
E1	3.00	±0.10
E2	2.95	±0.10
E3	0.51	±0.13
E4	0.51	±0.13
R	0.15	+0.15 -0.08
R1	0.15	+0.15 -0.08
t1	0.31	±0.08
t2	0.41	±0.08
b	0.33	+0.07 -0.06
b1	0.30	±0.05
c	0.18	±0.05
c1	0.15	+0.03 -0.02
Ø1	3.0°	±3.0°
Ø2	12.0°	±3.0°
Ø3	12.0°	±3.0°
L	0.55	±0.15
L1	0.95 BSC	_____
aaa	0.10	_____
bbb	0.08	_____
ccc	0.25	_____
e	0.65 BSC	_____
S	0.525 BSC	_____

SYMBOL	MINI SOIC 8LD - EP1	
	PACKAGE OUTLINE (MILLIMETER)	±TOL
A	1.10	MAX
A1	0.10	±0.05
A2	0.86	±0.08
D	3.00	±0.10
D2	2.95	±0.10
E	4.90	±0.15
E1	3.00	±0.10
E2	2.95	±0.10
E3	0.51	±0.13
E4	0.51	±0.13
R	0.15	+0.15 -0.08
R1	0.15	+0.15 -0.08
t1	0.31	±0.08
t2	0.41	±0.08
b	0.33	+0.07 -0.06
b1	0.30	±0.05
c	0.18	±0.05
c1	0.15	+0.03 -0.02
Ø1	3.0°	±3.0°
Ø2	12.0°	±3.0°
Ø3	12.0°	±3.0°
L	0.55	±0.15
L1	0.95 BSC	_____
aaa	0.10	_____
bbb	0.08	_____
ccc	0.25	_____
e	0.65 BSC	_____
S	0.525 BSC	_____
D3	2.18	±0.10
E5	1.73	±0.10

NOTES :

- ALL DIMENSIONS ARE IN MILLIMETERS (ANGLE IN DEGREES), UNLESS OTHERWISE SPECIFIED.
- DATUMS **-B-** AND **-C-** TO BE DETERMINED AT DATUM PLANE **-H-**.
- DIMENSIONS "D" AND "E1" ARE TO BE DETERMINED AT DATUM PLANE **-H-**.
- DIMENSIONS "D2" AND "E2" ARE FOR TOP PACKAGE AND DIMENSIONS "D" AND "E1" ARE FOR BOTTOM PACKAGE.
- CROSS SECTION A-A TO BE DETERMINED AT 0.13 TO 0.25 MM FROM THE LEAD TIP.
- DIMENSION "D" AND "D2" DOES NOT INCLUDE MOLD FLASH, PROTRUSION OR GATE BURRS.
- DIMENSION "E1" AND "E2" DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION.
- EXPOSED PAD FEATURE IS PRESENT ON VARIATIONS DESIGNATED WITH "-EP"

DWG NO		PKGSO00003		REV		J	
TOLERANCES (INCH/MM)				SCALE			
FRACTIONAL		DECIMALS		20:1			
±		X		ORIGINATOR			
±		XX		REQUESTOR			
±		XXX		TOOLING DEPT MANAGER			
±		XXXX		QA DEPT MANAGER			
±		XXXX		CTC DESIGN DEPT MANAGER			
FINISHING				TITLE			
SYMBOL				MINI SOIC 8LD			
SYMBOL				PACKAGE OUTLINE			
REF : CUSTOMER SPECIFICATION				SHEET NO : 2 OF 2			

